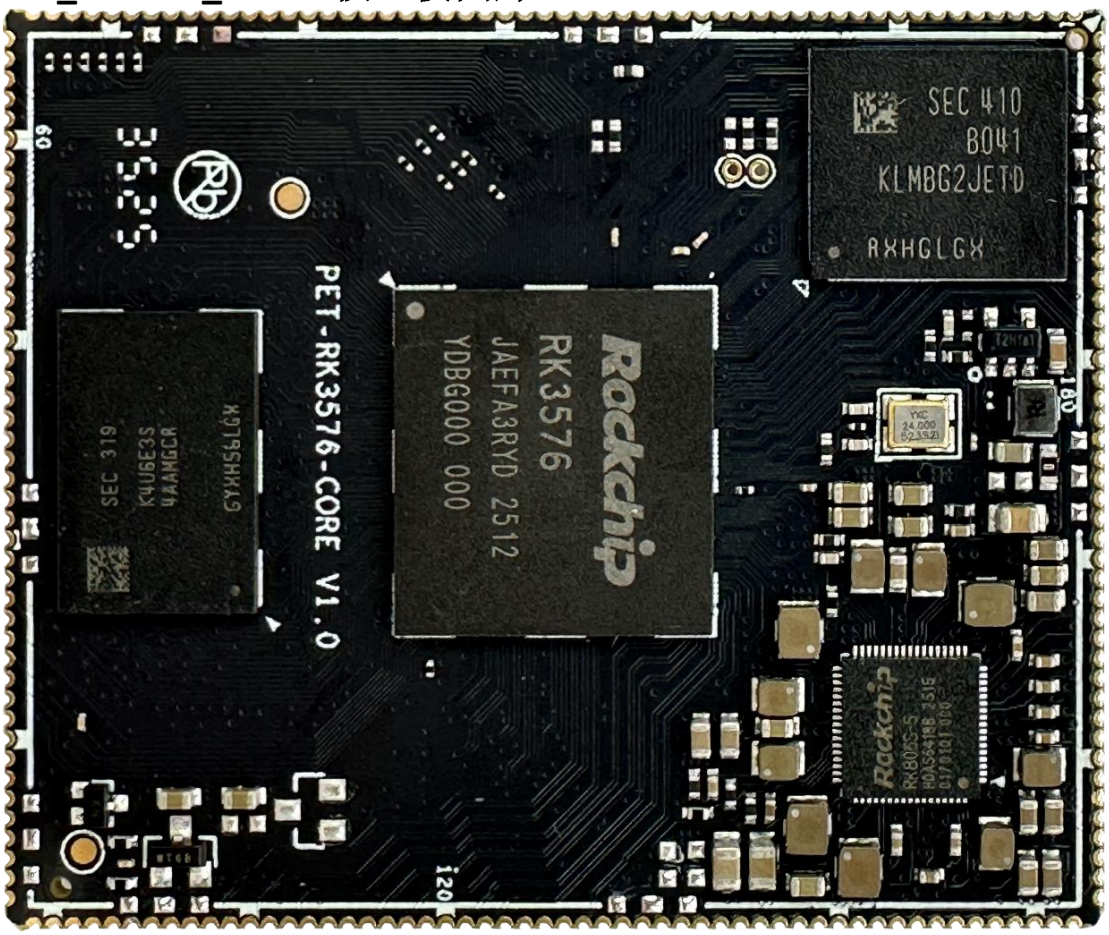
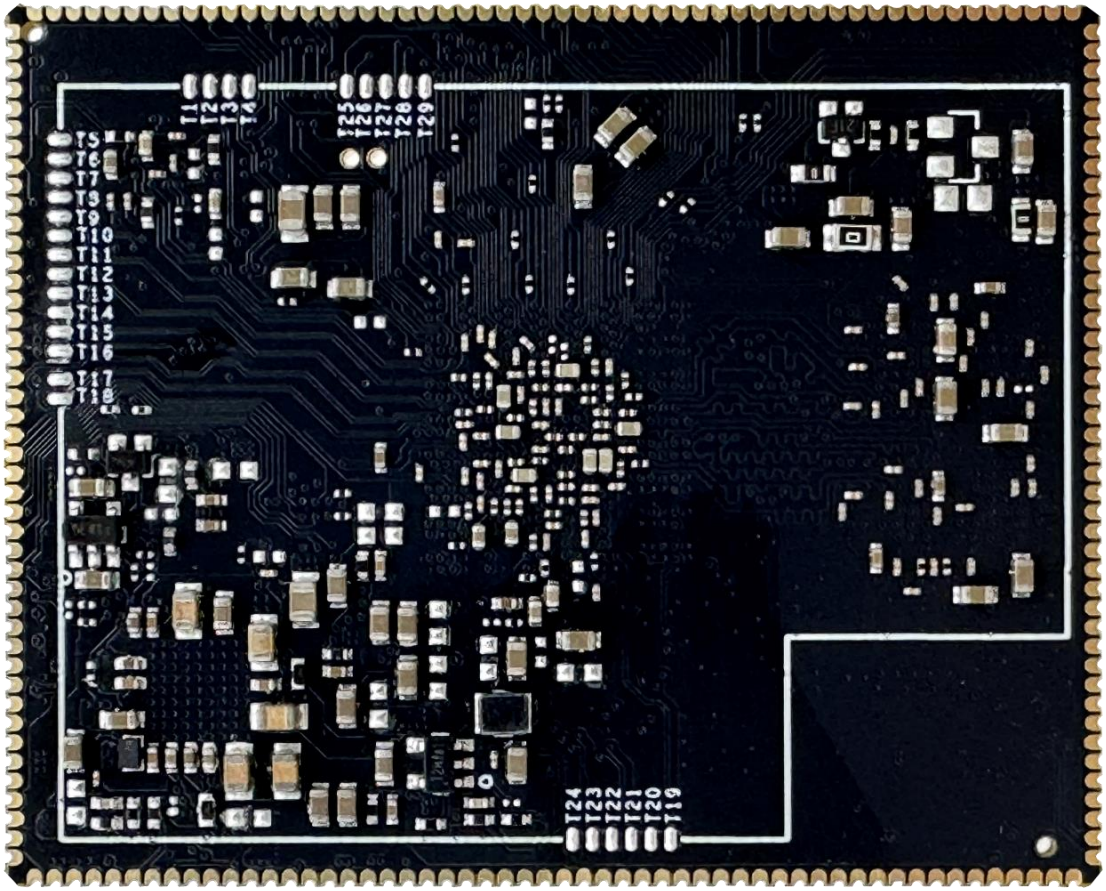
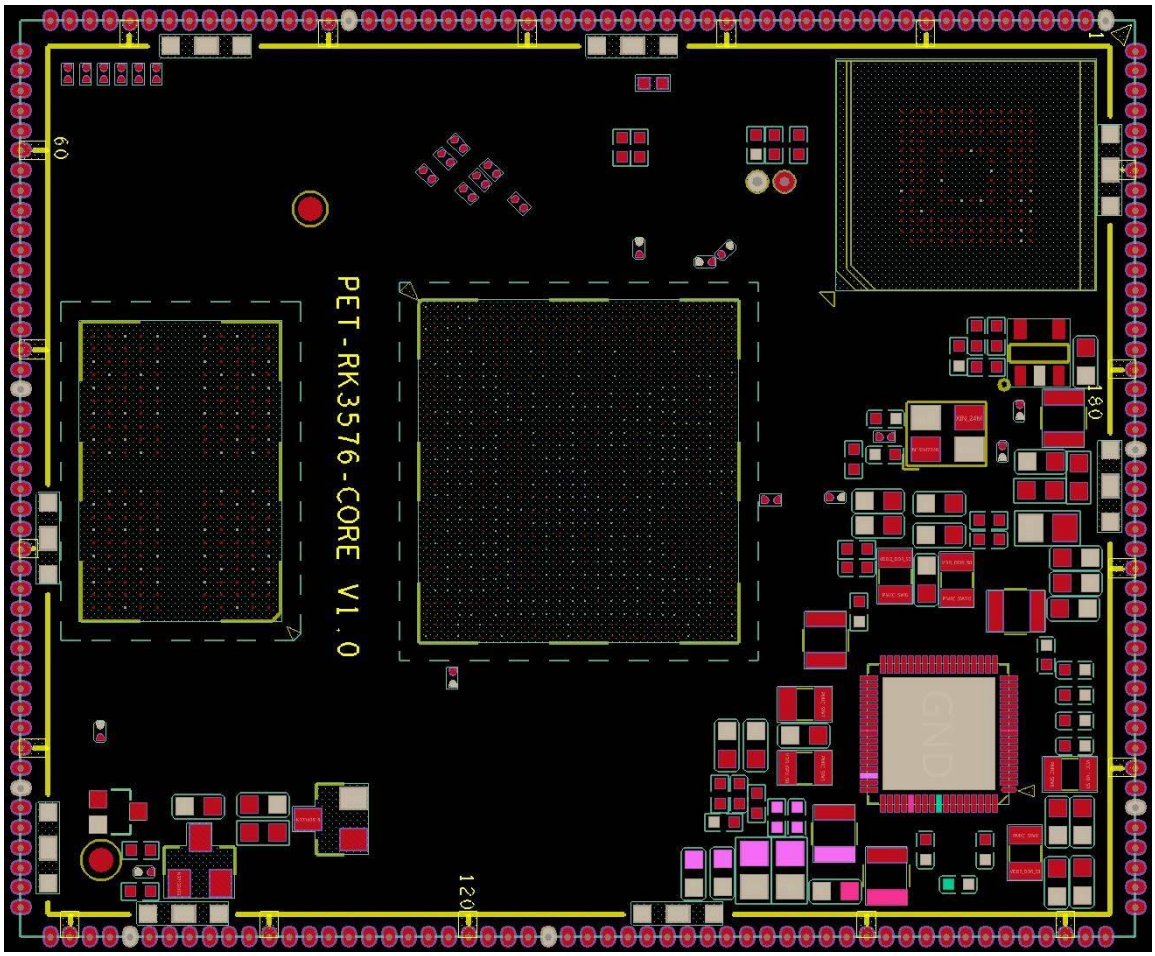
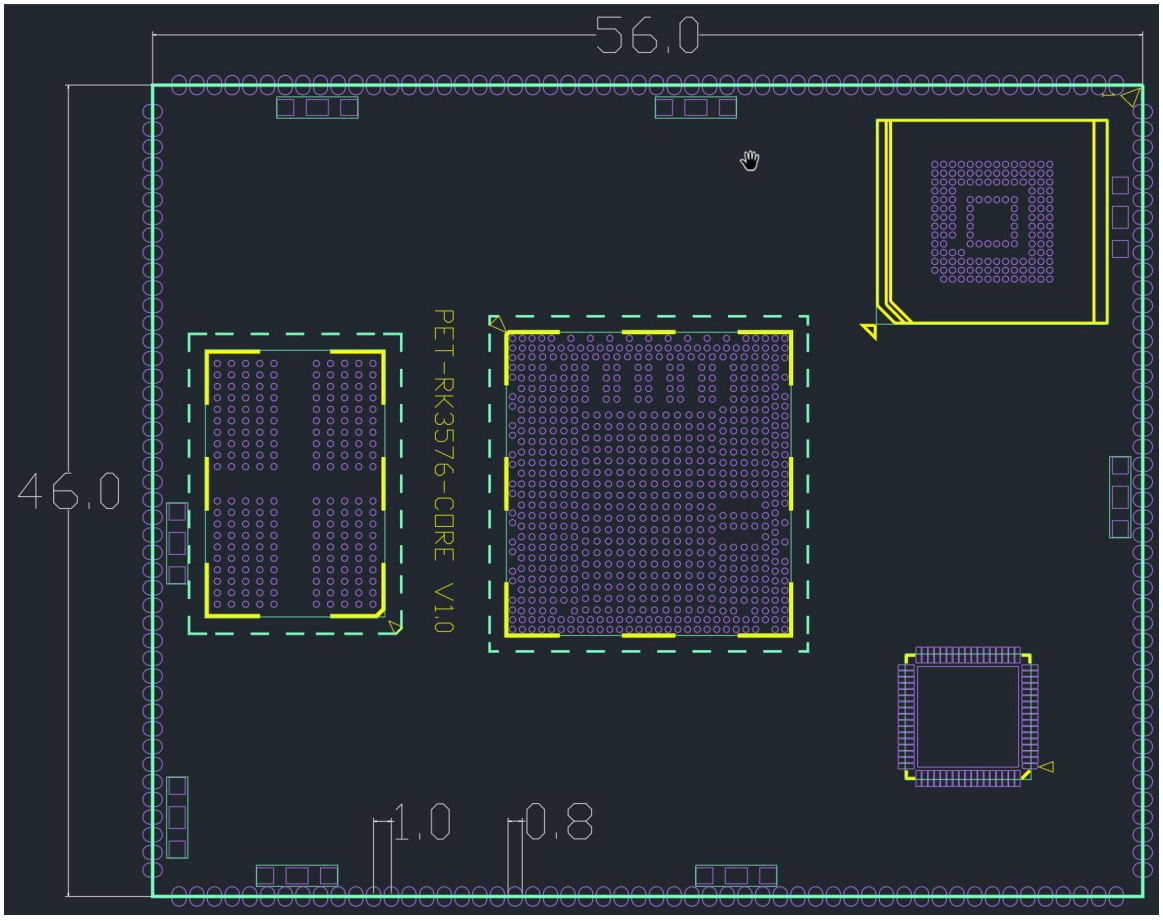
PET_RK3576_CORE 核心板
说明书

一、PET_RK3576_CORE 核心板图片

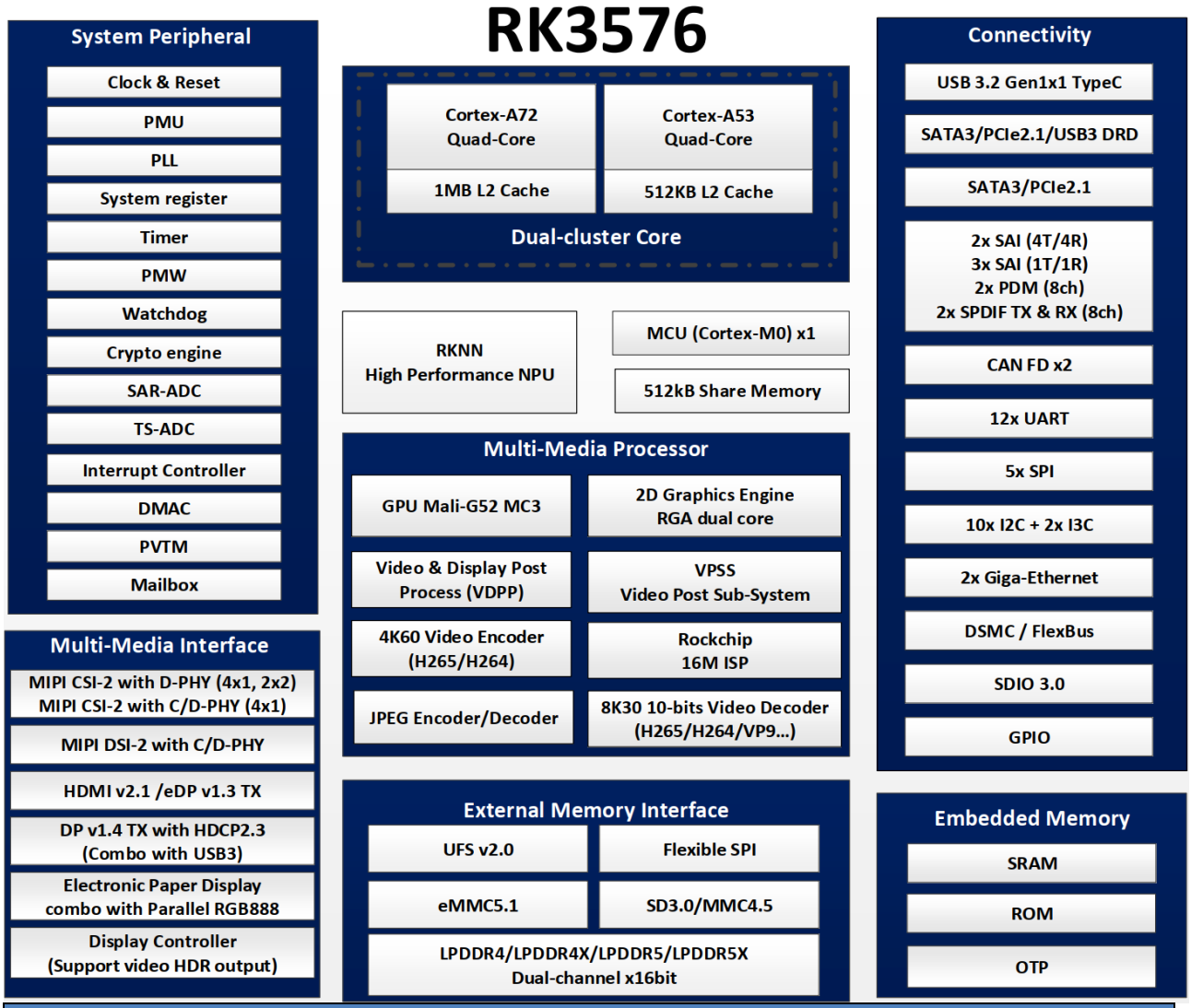





PET-RK3576-CORE V1.0
GND


56.0
46.0
PET-RK3576-CORE V1.0
1.0
0.8

二、PET_RK3576_CORE 核心板详细参数



PET-RK3576 核心板参数	
处理器	RK3576 四核 Cortex-A72 + 四核 Cortex-A53，64 位 ARM64，频率最高 2.2GHz
内置 NPU	RKNN NPU，6TOPS 算力，用于 AI 计算
内置 GPU	ARM G52 MC3，支持 OpenCL 2.0，OpenGLES 1.1、2.0、3.2，Vulkan1.1
内置视频编解码	支持 8K@30fps H265/H264 格式硬解码、4K@60fps 硬编码
内存 LPDDR4	2GB / 4GB / 8GB，标配 2GB
存储 EMMC	16GB / 32GB / 64GB / 128GB，标配 16GB
操作系统	支持安卓 Android 14、Buildroot Linux、Debian12、Ubuntu 24.04
PET-RK3576 核心板引脚功能	
LVDS 显示输出	单 8 单通道、双 8 双通道 1920x1080（通过底板 MIPI 转 LVDS 芯片实现）
HDMI 显示输出	最大支持分辨率 4096x2160
eDP 显示输出	最大支持分辨率 4096x2160
MIPI 显示输出	最大支持分辨率 2560x1600
RGB 显示输出	最大支持分辨率 1920x1080
DP 显示输出	最大支持分辨率 4096x2160

视频输入接口	MIPI CSI 2 路(8 Lane) ， 最大支持 1600 万像素
按键接口	电源键、烧写键、复位键、ADC 按键、GPIO 按键、GPIO 矩阵键盘
千兆以太网接口	2 路
USB 接口	1 路 USB3.0、2 路 USB2.0、OTG 1 路；用于外设、4G/5G 模组、USB 摄像头等
SDIO 接口	2 路，可用于 TF 卡、WIFI 蓝牙模组等
SPI 接口	5 路
SARADC 接口	8 路，10bit / 1MSPS / 12MHz 频率；用于 ADC 按键、ADC 输入检测
I2C 接口	10 路，用于电容触摸屏/RTC/NFC/I2C 外设等
UART 串口	12 路
I2S 接口	5 路
PWM 接口	16 路，用于 IR 红外输入、PWM 输出、显示屏背光控制等
CAN 接口	2 路
PCIE2.1 接口	2 路，1Lane；可用于 M2 电子盘、PCIE 外设等
SATA 接口	2 路，可用于 SATA 固态盘
看门狗	1 个，CPU 内置
SPDIF 输出	2 路，8 通道
PDM 输入	2 路，8 通道
GPIO/中断	多路
PET-RK3576 核心板结构参数	
尺寸	56 x 46 mm，厚度 1.2mm
邮票孔间距	1.0mm
引脚数	225 pin
PCB 工艺	10 层板，沉金工艺

注意：RK3576 引脚大部分是功能复用的，以上列表内的资源存在不能同时使用的情况，引脚功能复用情况可以查询下表或查看我司核心板精简版原理图。

三、PET_RK3576_CORE 核心板引脚详细说明

	名称	电平	SDK 默认功能	CPU 引脚可选配置功能
1	GND		电源地	
2	SARADC_VIN0_BOOT	1.8V	ROM 按键	SARADC_IN0_BOOT
3	CAN1_TX_M3	3.3V	CAN 总线	VI_CIF_CLKO/ETH1_PPSCLK_M0/ETH0_RXCTL_M1/SAI3_SDO_M2/SPDIF_RX1_M1/UART3_CTSN_M0/SPI3_MISO_M0/CAN1_TX_M3/MIPI_TE_M1/GPIO3_A2_d
4	CAN1_RX_M3	3.3V		VI_CIF_CLKI/ETH1_PTP_REFCLK_M0/ETH0_RXD1_M1/SAI3_SDI_M2/SPDIF_TX1_M1/UART3_RTSN_M0/SPI3_CSN0_M0/CAN1_RX_M3/GPIO3_A3_d
5	UART2_TX_M0	1.8V	UART2	ETH1_TXCTL_M1/FSPI1_D2_M1/PDM0_SDI0_M2/UART2_TX_M0/I2C8_SCL_M1/SATA_CPPOD/GPIO1_C6_d
6	UART2_RX_M0	1.8V		ETH1_RXD0_M1/FSPI1_D3_M1/PDM0_SDI1_M2/UART2_RX_M0/I2C8_SDA_M1/SATA_CPDET/GPIO1_C7_d
7	UART4_TX_M1	1.8V	UART4(BT)	ETH1_TXD0_M1/FSPI1_D0_M1/UART4_TX_M1/UART2_RTSN_M0/SPI2_MOSI_M1/PCIE0_BUTTONRSTN/GPIO1_C4_d
8	UART4_RX_M1	1.8V		ETH1_TXD1_M1/FSPI1_D1_M1/UART4_RX_M1/UART2_CTSN_M0/SPI2_MISO_M1/PCIE1_BUTTONRSTN/GPIO1_C5_d
9	UART4_CTSN_M1	1.8V		ETH1_PPSTRIG_M1/SDMMC1_DETN_M0/FSPI1_CSN0_M1/UART4_CTSN_M1/I2C6_SDA_M1/SPI2_CSN0_M1/GPIO1_C3_u
10	UART4_RTSN_M1	1.8V		ETH1_PPSCLK_M1/SDMMC1_PWREN_M0/FSPI1_RSTN_M1/FSPI1_CSN1_M1/UART4_RTSN_M1/I2C6_SCL_M1/SPI2_CSN1_M1/PWM1_CH2_M1/GPIO1_C2_u
11	UART10_TX_M1	1.8V	UART10	ETH1_RXD1_M1/SAI2_SDO_M0/UART10_TX_M1/GPIO1_D0_d
12	UART10_RX_M1	1.8V		ETH1_RXCTL_M1/SAI2_SCLK_M0/UART10_RX_M1/I3C0_SDA_PU_M1/GPIO1_D1_d
13	UART7_RX_M0	3.3V	UART7	VI_CIF_D6/ETH0_RXD2_M1/SAI0_LRCK_M0/UART7_RX_M0/UART8_CTSN_M1/I2C8_SDA_M2/GPIO2_B7_d
14	UART7_TX_M0	3.3V		VI_CIF_D7/ETH1_PTP_REFCLK_M1/ETH0_RXD3_M1/SAI0_SCLK_M0/UART7_TX_M0/UART8_RTSN_M1/I2C8_SCL_M2/GPIO2_B6_d
15	UART8_TX_M1	3.3V	UART8	VI_CIF_D15/SDMMC1_D0_M1/ETH0_RXD0_M1/SAI0_SDO0_M0/UART8_TX_M1/SPI4_CSN1_M3/I2C4_SCL_M2/GPIO2_A6_d
16	UART8_RX_M1	3.3V		VI_CIF_D14/SDMMC1_D1_M1/ETH0_TXCTL_M1/SAI0_SDO1_M0/UART8_RX_M1/I2C4_SDA_M2/GPIO2_A7_d
17	UART1_RX_M1	3.3V	UART1	VI_CIF_D12/SDMMC1_D3_M1/ETH0_TXD0_M1/SAI0_SDI1_M0/PDM0_SDI2_M3/UART1_RX_M1/GPIO2_B1_d
18	UART1_TX_M1	3.3V		VI_CIF_D13/SDMMC1_D2_M1/ETH0_TXD1_M1/SAI0_SDI0_M0/PDM0_SDI3_M3/UART1_TX_M1/GPIO2_B0_d

19	UART3_RX_M0	3.3V	UART3	VI_CIF_VSYNC/ETH1_PPSTRIG_M0/ETH0_MDC_M1/SAI3_LRCK_M2/UART3_RX_M0/SPI3_MOSI_M0/I2C7_SDA_M1/GPIO3_A1_d
20	UART3_TX_M0	3.3V		VI_CIF_HREF/ETH0_MDIO_M1/SAI3_SCLK_M2/UART3_TX_M0/SPI3_CLK_M0/I2C7_SCL_M1/GPIO3_A0_d
21	UART5_TX_M0	3.3V	UART5	VO_LCDC_HSYNC/VO_EBC_GDCLK/SAI1_SDI2_M1/DSMC_CLKP/FLEXBUS1_D0/UART5_TX_M0/SPI3_MISO_M1/I2C3_SDA_M2/GPIO3_D5_d
22	UART5_RX_M0	3.3V		VO_LCDC_DEN/VO_EBC_SDLE/SAI1_SDI1_M1/DSMC_DATA0/FLEXBUS1_D1/UART5_RX_M0/SPI3_CLK_M1/I2C3_SCL_M2/GPIO3_D4_d
23	UART11_TX_M0	3.3V	GPIO	VO_LCDC_D7/VO_EBC_SDDO7/SAI1_SDO1_M1/DSMC_DATA5/FLEXBUS1_D7/UART11_TX_M0/SPI2_CSN0_M2/I2C5_SCL_M3/CAN0_TX_M3/GPIO3_C4_d
24	UART11_RX_M0	3.3V	GPIO	VO_LCDC_D10/VO_EBC_SDDO10/ETH0_PTP_REFCLK_M0/SAI1_SDO2_M1/DSMC_DATA6/FLEXBUS1_D8/UART11_RX_M0/SPI2_MISO_M2/I2C5_SDA_M3/CAN0_RX_M3/GPIO3_C1_d
25	I2C4_SCL_M3_MIPI_CAM	3.3V	I2C4(Camera)	VO_LCDC_D11/VO_EBC_SDDO11/ETH0_PPSCLK_M0/SAI1_SDO3_M1/DSMC_DATA7/FLEXBUS1_D9/UART2_TX_M2/UART3_RTSN_M1/I2C4_SCL_M3/GPIO3_C0_d
26	I2C4_SDA_M3_MIPI_CAM	3.3V		VO_LCDC_D12/VO_EBC_SDDO12/ETH0_PPSTRIG_M0/SAI1_SDI0_M1/DSMC_DQS0/FLEXBUS1_D10/FLEXBUS1_CSN_M0/UART2_RX_M2/UART3_CTSN_M1/I2C4_SDA_M3/GPIO3_B7_d
27	MIPI_DPHY_CSI_CAM2_CLKOUT	3.3V	Camera 时钟	SPDIF_RX0_M1/CAM_CLK1_OUT_M0/SAI4_LRCK_M1/DSMC_INT0/FLEXBUS0_D13_M0/FLEXBUS1_D14_M0/FLEXBUS1_CSN_M3/UART3_TX_M1/SPI1_CSN1_M2/I2C7_SCL_M2/MIPI_TE_M2/GPIO4_A0_d
28	GPIO3_D6_d	3.3V	GPIO(4G 复位)	VO_LCDC_VSYNC/VO_EBC_SDCLK/SAI1_SDI3_M1/DSMC_CLKN/FLEXBUS1_CLK/UART5_CTSN_M0/SPI3_MOSI_M1/PWM2_CH6_M3/GPIO3_D6_d
29	GPIO3_C5_d	3.3V	GPIO(Camera)	VO_LCDC_D6/VO_EBC_SDDO6/SAI1_SDO0_M1/DSMC_DATA4/FLEXBUS1_D6/UART8_RX_M0/SPI1_MISO_M2/PWM2_CH2_M3/GPIO3_C5_d
30	GPIO4_A1_d	3.3V	GPIO(NFC)	VO_POST_EMPTY/SPDIF_TX0_M1/CAM_CLK2_OUT_M0/SAI4_SDO_M1/DSMC_INT2/FLEXBUS0_D14_M0/FLEXBUS1_D13_M0/FLEXBUS0_CSN_M1/UART3_RX_M1/I2C7_SDA_M2/GPIO4_A1_d
31	GPIO3_C7_d	3.3V	GPIO(Camera)	VO_LCDC_D4/VO_EBC_SDDO4/SAI1_SCLK_M1/DSMC_DATA2/FLEXBUS1_D4/UART8_RTSN_M0/SPI1_CLK_M2/GPIO3_C7_d
32	GPIO3_C6_d	3.3V	GPIO(Camera)	VO_LCDC_D5/VO_EBC_SDDO5/SAI1_LRCK_M1/DSMC_DATA3/FLEXBUS1_D5/UART8_TX_M0/SPI1_MOSI_M2/GPIO3_C6_d
33	MIPI_DCPHY_CSI_CAM1_CLKOUT	3.3V	Camera 时钟	VO_LCDC_CLK/VO_EBC_SDOE/CAM_CLK0_OUT_M0/SAI4_SCLK_M1/DSMC_RESETN/FLEXBUS0_D15_M0/FLEXBUS1_D12_M0/FLEXBUS1_CSN_M1/UART5_RTSN_M0/SPI3_CSN1_M1/PWM2_CH7_M3/GPIO3_D7_d

34	GPIO4_A7_d	3.3V	GPIO(GMAC0)	SAI1_SDO0_M0/SAI4_SDI_M0/SPI3_CLK_M2/PWM2_CH6_M0/GPIO4_A7_d
35	GPIO4_B0_d	3.3V	GPIO(GMAC1)	SAI1_SDO1_M0/SAI1_SDI3_M0/PDM1_CLK1_M1/FLEXBUS1_D13_M1/SPI4_CLK_M2/UART5_TX_M1/UART6_RTSN_M0/UART2_RTSN_M1/GPIO4_B0_d
36	GPIO4_B2_d	3.3V	GPIO	SAI1_SDO3_M0/SAI1_SDI1_M0/PDM1_SDI1_M1/FLEXBUS1_D15_M1/SPI4_MISO_M2/MIPI_TE_M0/GPIO4_B2_d
37	I2C3_SDA_M0_Audio	3.3V	I2C3(NFC)	SPDIF_RX0_M0/FLEXBUS0_CSN_M4/UART2_RX_M1/I2C3_SDA_M0/CAN1_RX_M2/GPIO4_B4_d
38	I2C3_SCL_M0_Audio	3.3V		SPDIF_TX0_M0/FLEXBUS0_D15_M1/UART2_TX_M1/I2C3_SCL_M0/PCIE0_CLKREQN_M2/CAN1_TX_M2/GPIO4_B5_d
39	GND		电源地	
40	ETH_CLK1_25M_OUT_M0	3.3V	GMAC1 时钟	CAM_CLK1_OUT_M1/ETH_CLK1_25M_OUT_M0/ETH0_MCLK_M1/SAI3_MCLK_M2/SPDIF_RX0_M2/UART9_RTSN_M0/I3C1_SDA_PU_M0/PWM2_CH6_M2/GPIO2_D6_d
41	GMAC1_RXCLK_M0	3.3V	GMAC1	VI_CIF_D3/ETH1_RXCLK_M0/ETH0_PPSTRIG_M1/SAI2_SCLK_M1/PDM1_SDI2_M0/UART11_CTSN_M1/SPI1_MOSI_M1/PWM1_CH2_M2/GPIO2_C2_d
42	GMAC1_RXCTL_M0	3.3V		ETH1_RXCTL_M0/UART6_RX_M1/I3C1_SDA_M0/PWM2_CH3_M2/GPIO2_D3_d
43	GMAC1_RXD0_M0	3.3V		ETH1_RXD0_M0/SAI4_SDO_M3/UART4_RX_M0/I2C6_SDA_M2/PWM2_CH1_M2/GPIO2_D1_d
44	GMAC1_RXD1_M0	3.3V		CAM_CLK0_OUT_M1/ETH1_RXD1_M0/SAI4_MCLK_M3/UART6_TX_M1/I3C1_SCL_M0/PWM2_CH2_M2/GPIO2_D2_d
45	GMAC1_RXD2_M0	3.3V		VI_CIF_D5/ETH1_RXD2_M0/ETH0_PTP_REFCLK_M1/PDM1_SDI1_M0/UART9_RX_M0/PWM1_CH0_M2/GPIO2_C0_d
46	GMAC1_RXD3_M0	3.3V		VI_CIF_D4/ETH1_RXD3_M0/ETH0_PPSCLK_M1/SAI2_MCLK_M1/PDM1_CLK1_M0/UART9_TX_M0/SPI1_CSN1_M1/PWM1_CH1_M2/GPIO2_C1_d
47	GMAC1_TXCLK_M0	3.3V		VI_CIF_D0/ETH1_TXCLK_M0/SAI2_SDI_M1/PDM1_CLK0_M0/UART11_RX_M1/SPI1_CLK_M1/PWM1_CH4_M2/GPIO2_C5_d
48	GMAC1_TXCTL_M0	3.3V		ETH1_TXCTL_M0/SAI4_SDI_M3/UART4_TX_M0/I2C6_SCL_M2/PWM2_CH0_M2/GPIO2_D0_d
49	GMAC1_TXD0_M0	3.3V		ETH1_TXD0_M0/SAI4_SCLK_M3/UART4_CTSN_M0/I2C5_SCL_M2/PWM1_CH5_M2/GPIO2_C6_d
50	GMAC1_TXD1_M0	3.3V		ETH1_TXD1_M0/SAI4_LRCK_M3/UART4_RTSN_M0/I2C5_SDA_M2/PWM0_CH1_M2/GPIO2_C7_d
51	GMAC1_TXD2_M0	3.3V		VI_CIF_D2/ETH1_TXD2_M0/SAI2_LRCK_M1/PDM1_SDI3_M0/UART11_RTSN_M1/SPI1_MISO_M1/PWM0_CH0_M2/GPIO2_C3_d
52	GMAC1_TXD3_M0	3.3V		VI_CIF_D1/ETH1_TXD3_M0/SAI2_SDO_M1/PDM1_SDI0_M0/UART11_TX_M1/SPI1_CSN0_M1/PWM1_CH3_M2/GPIO2_C4_d

53	GMAC1_MDIO_M0	3.3V		ISP_FLASH_TRIGOUT_M0/ETH1_MDIO_M0/UART6_CTSN_M1/I2C9_SCL_M2/PWM2_CH5_M2/GPIO2_D5_d
54	GMAC1_MDC_M0	3.3V		ISP_PRELIGHT_TRIG_M0/ETH1_MDC_M0/UART6_RTSN_M1/I2C9_SDA_M2/PWM2_CH4_M2/GPIO2_D4_d
55	ETH_CLK0_25M_OUT_M0	3.3V	GMAC0 时钟	VO_LCDC_D23/VO_EBC_SDSHR/ETH_CLK0_25M_OUT_M0/SAI4_SDI_M1/DSMC_RDYN/FLEXBUS1_D11/FLEXBUS0_CSN_M0/UART1_CTSN_M2/SPI2_CLK_M2/PWM1_CH0_M3/GPIO3_A4_d
56	GMAC0_RXCLK_M0	3.3V	GMAC0	VO_LCDC_D2/VO_EBC_SDDO2/ETH0_RXCLK_M0/SAI2_MCLK_M2/DSMC_CSN2/FLEXBUS0_D11/FLEXBUS1_CSN_M2/SPI4_CLK_M1/I3C1_SDA_PU_M2/GPIO3_D1_d
57	GMAC0_RXCTL_M0	3.3V		VO_LCDC_D20/VO_EBC_VCOM/ETH0_RXCTL_M0/PDM1_CLK1_M2/DSMC_DATA13/FLEXBUS0_D5/UART1_TX_M2/UART10_RTSN_M0/GPIO3_A7_d
58	GMAC0_RXD0_M0	3.3V		VO_LCDC_D17/VO_EBC_SDCE1/ETH0_RXD0_M0/PDM1_SDI1_M2/DSMC_DATA11/FLEXBUS0_D3/UART9_RX_M1/I2C8_SDA_M3/GPIO3_B2_d
59	GMAC0_RXD1_M0	3.3V		VO_LCDC_D18/VO_EBC_SDCE2/ETH0_RXD1_M0/PDM1_CLK0_M2/DSMC_DATA12/FLEXBUS0_D4/UART10_TX_M0/SPI4_CSN0_M1/PWM1_CH3_M3/GPIO3_B1_d
60	GMAC0_RXD2_M0	3.3V		VO_LCDC_D0/VO_EBC_SDDO0/ETH0_RXD2_M0/SAI2_SDO_M2/DSMC_CSN0/FLEXBUS1_D2/UART2_CTSN_M2/I3C1_SCL_M2/PWM2_CH5_M3/GPIO3_D3_d
61	GMAC0_RXD3_M0	3.3V		VO_LCDC_D1/VO_EBC_SDDO1/ETH0_RXD3_M0/SAI2_SDI_M2/DSMC_CSN3/FLEXBUS0_D12/FLEXBUS1_D15_M0/FLEXBUS0_CSN_M3/UART2_RTSN_M2/SPI4_CSN1_M1/I3C1_SDA_M2/PWM2_CH4_M3/GPIO3_D2_d
62	GMAC0_TXCLK_M0	3.3V		VO_LCDC_D13/VO_EBC_SDDO13/ETH0_TXCLK_M0/DSMC_DQS1/FLEXBUS0_CLK/SPI3_CSN0_M1/PWM0_CH1_M3/GPIO3_B6_d
63	GMAC0_TXCTL_M0	3.3V		VO_LCDC_D16/VO_EBC_SDCE0/ETH0_TXCTL_M0/PDM1_SDI0_M2/DSMC_DATA10/FLEXBUS0_D2/UART9_TX_M1/I2C8_SCL_M3/GPIO3_B3_d
64	GMAC0_TXD0_M0	3.3V		VO_LCDC_D14/VO_EBC_SDDO14/ETH0_TXD0_M0/SPDIF_TX1_M0/DSMC_DATA8/FLEXBUS0_D0/UART9_CTSN_M1/PWM1_CH5_M3/GPIO3_B5_d
65	GMAC0_TXD1_M0	3.3V		VO_LCDC_D15/VO_EBC_SDDO15/ETH0_TXD1_M0/SPDIF_RX1_M0/DSMC_DATA9/FLEXBUS0_D1/UART9_RTSN_M1/PWM1_CH4_M3/GPIO3_B4_d
66	GMAC0_TXD2_M0	3.3V		VO_LCDC_D8/VO_EBC_SDDO8/ETH0_TXD2_M0/SAI2_LRCK_M2/DSMC_INT3/FLEXBUS0_D10/FLEXBUS0_CSN_M2/UART11_CTSN_M0/SPI4_MOSI_M1/I2C9_SDA_M3/PWM2_CH1_M3/GPIO3_C3_d
67	GMAC0_TXD3_M0	3.3V		VO_LCDC_D9/VO_EBC_SDDO9/ETH0_TXD3_M0/SAI2_SCLK_M2/DSMC_INT1/FLEXBUS0_D9/UART11_RTSN_M0/SPI4_MISO_M1/I2C9_SCL_M3/PWM2_CH0_M3/GPIO3_C2_d
68	GMAC0_MDIO_M0	3.3V		VO_LCDC_D22/VO_EBC_GDSP/ETH0_MDIO_M0/PDM1_SDI3_M2/DSMC_DATA15/FLEXBUS0_D7/UART1_RTSN

				_M2/SPI2_CSN1_M2/PWM1_CH1_M3/GPIO3_A5_d
69	GMAC0_MDC_M0	3.3V		VO_LCDC_D21/VO_EBC_GDOE/ETH0_MDC_M0/PDM1_SDI2_M2/DSMC_DATA14/FLEXBUS0_D6/UART1_RX_M2/UART10_CTSN_M0/PWM1_CH2_M3/GPIO3_A6_d
70	GPIO4_A4_d	3.3V	GPIO	SAI4_SCLK_M0/PDM1_SDI3_M1/FLEXBUS0_D13_M1/SPI3_MOSI_M2/UART6_TX_M0/I2C4_SCL_M1/CAN0_TX_M2/GPIO4_A4_d
71	GPIO4_A6_d	3.3V	GPIO	SAI4_LRCK_M0/PDM1_CLK0_M1/FLEXBUS0_D14_M1/SPI3_MISO_M2/UART6_RX_M0/I2C4_SDA_M1/CAN0_RX_M2/GPIO4_A6_d
72	GND		电源地	
73	GPIO2_D7_d	3.3V	GPIO(功放开关)	CAM_CLK2_OUT_M1/ETH1_MCLK_M0/ETH_CLK0_25M_OUT_M1/SAI0_SDO3_M0/SPDIF_TX0_M2/UART9_CTSN_M0/SPI3_CSN1_M0/PWM2_CH7_M2/GPIO2_D7_d
74	GPIO2_B5_d	3.3V	GPIO(耳机检测)	VI_CIF_D8/SDMMC1_DETN_M1/ETH0_RXCLK_M1/SAI0_MCLK_M0/PDM0_CLK0_M3/UART7_RTSN_M0/SPI4_MISO_M3/SATA1_ACTLED_M0/GPIO2_B5_d
75	SARADC_VIN3	1.8V	ADC	SARADC_IN3
76	SAI1_SDI0_M0	3.3V	I2S1(CODEC)	SAI1_SDI0_M0/SAI4_SDO_M0/PDM1_SDI0_M1/SPI4_CSN0_M2/SPI3_CSN1_M2/PWM2_CH7_M0/GPIO4_B3_d
77	SAI1_SDO2_M0	3.3V		SAI1_SDO2_M0/SAI1_SDI2_M0/PDM1_SDI2_M1/FLEXBUS1_D14_M1/SPI4_MOSI_M2/UART5_RX_M1/UART6_CTSN_M0/UART2_CTSN_M1/GPIO4_B1_d
78	SAI1_LRCK_M0	3.3V		SAI1_LRCK_M0/FLEXBUS1_D12_M1/SPI4_CSN1_M2/UART5_CTSN_M1/I2C2_SDA_M2/PCIE1_CLKREQN_M2/GPIO4_A5_d
79	SAI1_SCLK_M0	3.3V		SAI1_SCLK_M0/FLEXBUS1_CSN_M4/SPI3_CSN0_M2/UART5_RTSN_M1/I2C2_SCL_M2/PWM2_CH4_M1/GPIO4_A3_d
80	SAI1_MCLK_M0	3.3V		SAI1_MCLK_M0/SAI4_MCLK_M0/AUPLL_CLK_IN_M2/PWM2_CH5_M0/GPIO4_A2_d
81	UART6_RX_M3	3.3V	UART6	ISP_FLASH_TRIGOUT_M1/SAI4_SDO_M2/VP0_SYNC_OUT/SATA1_ACTLED_M1/I2C3_SDA_M3/SPI4_MOSI_M0/UART6_RX_M3/PWM2_CH5_M1/GPIO4_C5_d
82	UART6_TX_M3	3.3V		ISP_PRELIGHT_TRIG_M1/SAI4_LRCK_M2/DP_HPDIN_M0/I2C3_SCL_M3/SPI4_CSN0_M0/UART6_TX_M3/PWM2_CH6_M1/GPIO4_C4_d
83	GPIO4_C6_d	3.3V	GPIO(HDMI)	SAI4_SDI_M2/VP1_SYNC_OUT/PCIE0_CLKREQN_M3/SATA0_ACTLED_M1/I2C6_SCL_M3/SPI4_MISO_M0/CAN1_TX_M1/PWM2_CH2_M1/GPIO4_C6_d
84	HDMI_TX_SDA	3.3V	HDMI 输出	DSM_AUD_RN_M1/HDMI_TX_SDA/I2C2_SDA_M3/CAN0_RX_M1/UART9_RX_M2/PWM2_CH1_M1/GPIO4_C3_d
85	HDMI_TX_SCL	3.3V		DSM_AUD_RP_M1/HDMI_TX_SCL/I2C2_SCL_M3/CAN0_TX_M1/UART9_TX_M2/PWM2_CH0_M1/GPIO4_C2_d

86	HDMI_TX_HPDIN_M0	3.3V		DSM_AUD_LN_M1/HDMI_TX_HPDIN_M0/PCIE1_CLKREQN_M3/I2C7_SDA_M3/EDP_TX_HPDIN_M0/UART11_RX_M2/PWM0_CH1_M1/GPIO4_C1_d
87	HDMI_TX_CEC_M0	3.3V		DSM_AUD_LP_M1/SAI4_MCLK_M2/HDMI_TX_CEC_M0/I2C7_SCL_M3/SPI4_CSN1_M0/UART11_TX_M2/PWM1_CH5_M1/GPIO4_C0_d
88	I2C0_SCL_M1_TP	3.3V	I2C0(TP)	I2C0_SCL_M1/UART8_TX_M2/I3C0_SCL_M0/GPIO0_C1_d
89	I2C0_SDA_M1_TP	3.3V		I2C0_SDA_M1/UART8_RX_M2/I3C0_SDA_M0/GPIO0_C2_d
90	GPIO4_C7_d	3.3V	GPIO(指示灯)	SAI4_SCLK_M2/VP2_SYNC_OUT/I2C6_SDA_M3/SPI4_CLK_M0/CAN1_RX_M1/PWM2_CH3_M1/GPIO4_C7_d
91	GPIO4_D0_d	1.8V	GPIO(风扇)	UFS_RSTN/GPIO4_D0_d
92	GND		电源地	
93	USB2_OTG0_DP		USB3.0 USB2.0 OTG	USB2_OTG0_DP
94	USB2_OTG0_DM			USB2_OTG0_DM
95	USB2_HOST1_DP			USB2_OTG1_DP
96	USB2_HOST1_DM			USB2_OTG1_DM
97	USB2_OTG0_ID			USB2_OTG0_ID
98	USB2_OTG0_VBUSDET			USB2_OTG0_VBUSDET
99	USB3_OTG0_SSRX1N			USB3_OTG0_SSRX1N/DP_TX_D0N
100	USB3_OTG0_SSRX1P			USB3_OTG0_SSRX1P/DP_TX_D0P
101	USB3_OTG0_SSTX1P			USB3_OTG0_SSTX1P/DP_TX_D1P
102	USB3_OTG0_SSTX1N			USB3_OTG0_SSTX1N/DP_TX_D1N
103	GND		电源地	
104	MIPI_DPHY_DSI_TX_D0N	1.8V	MIPI 显示屏	MIPI_DPHY_DSI_TX_D0N/MIPI_CPHY_DSI_TX_TRIO0_A
105	MIPI_DPHY_DSI_TX_D0P	1.8V		MIPI_DPHY_DSI_TX_D0P/MIPI_CPHY_DSI_TX_TRIO0_B
106	MIPI_DPHY_DSI_TX_D1N	1.8V		MIPI_DPHY_DSI_TX_D1N/MIPI_CPHY_DSI_TX_TRIO0_C
107	MIPI_DPHY_DSI_TX_D1P	1.8V		MIPI_DPHY_DSI_TX_D1P/MIPI_CPHY_DSI_TX_TRIO1_A
108	MIPI_DPHY_DSI_TX_CLKN	1.8V		MIPI_DPHY_DSI_TX_CLKN/MIPI_CPHY_DSI_TX_TRIO1_B
109	MIPI_DPHY_DSI_TX_CLKP	1.8V		MIPI_DPHY_DSI_TX_CLKP/MIPI_CPHY_DSI_TX_TRIO1_C
110	MIPI_DPHY_DSI_TX_D2N	1.8V		MIPI_DPHY_DSI_TX_D2N/MIPI_CPHY_DSI_TX_TRIO2_A
111	MIPI_DPHY_DSI_TX_D2P	1.8V		MIPI_DPHY_DSI_TX_D2P/MIPI_CPHY_DSI_TX_TRIO2_B

112	MIPI_DPHY_DSI_TX_D3N	1.8V		MIPI_DPHY_DSI_TX_D3N/MIPI_CPHY_DSI_TX_TRIO2_C
113	MIPI_DPHY_DSI_TX_D3P	1.8V		MIPI_DPHY_DSI_TX_D3P
114	HDMI_TX_SBDN	1.8V	HDMI 输出	HDMI_TX_SBDN/EDP_TX_AUXN
115	HDMI_TX_SBDP	1.8V		HDMI_TX_SBDP/EDP_TX_AUXP
116	HDMI_TX_D3N	1.8V		HDMI_TX_D3N/EDP_TX_D3N
117	HDMI_TX_D3P	1.8V		HDMI_TX_D3P/EDP_TX_D3P
118	HDMI_TX_D0N	1.8V		HDMI_TX_D0N/EDP_TX_D0N
119	HDMI_TX_D0P	1.8V		HDMI_TX_D0P/EDP_TX_D0P
120	HDMI_TX_D1N	1.8V		HDMI_TX_D1N/EDP_TX_D1N
121	HDMI_TX_D1P	1.8V		HDMI_TX_D1P/EDP_TX_D1P
122	HDMI_TX_D2N	1.8V		HDMI_TX_D2N/EDP_TX_D2N
123	HDMI_TX_D2P	1.8V		HDMI_TX_D2P/EDP_TX_D2P
124	GND		电源地	
125	MIPI_DPHY_CSI2_RX_CLKP	1.8V	MIPI Camera	MIPI_DPHY_CSI2_RX_CLKP
126	MIPI_DPHY_CSI2_RX_CLKN	1.8V		MIPI_DPHY_CSI2_RX_CLKN
127	MIPI_DPHY_CSI1_RX_D3N	1.8V		MIPI_DPHY_CSI1_RX_D3N/MIPI_DPHY_CSI2_RX_D1N
128	MIPI_DPHY_CSI1_RX_D3P	1.8V		MIPI_DPHY_CSI1_RX_D3P/MIPI_DPHY_CSI2_RX_D1P
129	MIPI_DPHY_CSI1_RX_CLKN	1.8V		MIPI_DPHY_CSI1_RX_CLKN
130	MIPI_DPHY_CSI1_RX_CLKP	1.8V		MIPI_DPHY_CSI1_RX_CLKP
131	MIPI_DPHY_CSI1_RX_D1N	1.8V		MIPI_DPHY_CSI1_RX_D1N
132	MIPI_DPHY_CSI1_RX_D1P	1.8V		MIPI_DPHY_CSI1_RX_D1P
133	MIPI_DPHY_CSI1_RX_D2N	1.8V		MIPI_DPHY_CSI1_RX_D2N/MIPI_DPHY_CSI2_RX_D0N
134	MIPI_DPHY_CSI1_RX_D2P	1.8V		MIPI_DPHY_CSI1_RX_D2P/MIPI_DPHY_CSI2_RX_D0P
135	MIPI_DPHY_CSI1_RX_D0N	1.8V		MIPI_DPHY_CSI1_RX_D0N
136	MIPI_DPHY_CSI1_RX_D0P	1.8V		MIPI_DPHY_CSI1_RX_D0P
137	GPIO0_C5_d	3.3V	GPIO(LCD 复位)	SAI0_SDO0_M1/DP_HPDIN_M1/UART10_RX_M2/I3C0_SDA_PU_M0/GPIO0_C5_d
138	GPIO0_D3_d	3.3V	GPIO(NFC)	SAI0_SDI3_M1/SAI0_SDO1_M1/PDM0_SDI3_M0/I2C4_SDA_M0/GPU_AVS/PWM2_CH0_M0/UART1_RTSN_M0/GPIO0_D3_d

139	GPIO0_D2_d	3.3V	GPIO(NFC)	SAI0_SDI2_M1/SAI0_SDO2_M1/PDM0_SDI2_M0/I2C4_SCL_M0/CPUBIG_AVS/PWM1_CH5_M0/UART1_CTSN_M0/GPIO0_D2_d
140	SPI0_CLK_M0	3.3V	GPIO	SAI0_LRCK_M1/I2C3_SDA_M1/SPI0_CLK_M0/GPIO0_C7_d
141	SPI0_CSN0_M0	3.3V	GPIO	SAI0_SCLK_M1/I2C3_SCL_M1/SPI0_CSN0_M0/GPIO0_C6_d
142	SPI0_MISO_M0	3.3V	GPIO	SAI0_SDI1_M1/SAI0_SDO3_M1/PDM0_SDI1_M0/SPI0_MISO_M0/GPIO0_D1_d
143	SPI0_CSN1_M0	3.3V	GPIO	PDM0_CLK1_M0/HDMI_TX_CEC_M1/SPI0_CSN1_M0/PWM0_CH1_M0/GPIO0_C3_d
144	SPI0_MOSI_M0	3.3V	GPIO	SAI0_SDI0_M1/PDM0_SDI0_M0/SPI0_MOSI_M0/GPIO0_D0_d
145	LCD_BL_PWM1_CH1_M0	3.3V	PWM1(LCD)	REF_CLK2_OUT/I2C1_SDA_M1/UART4_RX_M2/PWM1_CH1_M0/GPIO0_B5_d
146	PWM0_CH0_M0_IR	3.3V	PWM0(IR)	SAI0_MCLK_M1/PDM0_CLK0_M0/UART10_TX_M2/PWM0_CH0_M0/GPIO0_C4_d
147	RESET_L	1.8V	复位按键	
148	RK806_VDC	1.8V	上电开机信号	
149	GPIO0_A5_d	1.8V	GPIO(RTC)	PWR_CTRL3/I2C6_SDA_M0/GPIO0_A5_d
150	32KOUT_RTC2SOC	1.8V	32.768K 输入	CLK_32K_IN/CLK0_32K_OUT/I2C6_SCL_M0/GPIO0_A2_d
151	GPIO0_B0_z	1.8V	GPIO(WIFI)	AUPLL_CLK_IN_M1/SPI2_CSN0_M0/I2C0_SCL_M0/GPIO0_B0_z
152	GPIO0_B1_z	1.8V	GPIO(BT)	SPI2_MISO_M0/I2C0_SDA_M0/GPIO0_B1_z
153	VCC5V0_SYS_S5		5V 电源输入	
154	VCC_3V3_S3		3.3V 电源输出	
155	VCC_1V8_S3		1.8V 电源输出	
156	VCC_1V8_S0		1.8V 电源输出	
157	VCC_3V3_S0		3.3V 电源输出	
158	GND		电源地	
159	PWRON_L	1.8V	电源按键	
160	GPIO1_D4_d	1.8V	GPIO(BT)	ETH1_MCLK_M1/SAI2_MCLK_M0/PDM0_SDI3_M2/SPDIF_RX1_M2/UART10_RTSN_M1/I2C5_SCL_M1/GPIO1_D4_d
161	CLK1_32K_OUT_WIFI	1.8V	32.768K 输出	ETH_CLK1_25M_OUT_M1/FSPI1_CLK_M1/PDM0_CLK1_M2/SPDIF_TX1_M2/UART10_CTSN_M1/I2C5_SDA_M1/SPI2_CLK_M1/SATA_MPSWIT/CLK1_32K_OUT/GPIO1_D5_d
162	GPIO1_D2_d	1.8V	GPIO(WIFI)	ETH1_MDC_M1/SAI2_LRCK_M0/I3C0_SCL_M1/PWM1_CH3_M1/GPIO1_D2_d
163	GPIO1_D3_d	1.8V	GPIO(BT)	ETH1_MDIO_M1/SAI2_SDI_M0/I3C0_SDA_M1/PWM1_CH4_M1/GPIO1_D3_d

164	GPIO3_D0_d	3.3V	GPIO(Camera)	VO_LCDC_D3/VO_EBC_SDDO3/SAI1_MCLK_M1/DSMC_DATA1/FLEXBUS1_D3/UART8_CTSN_M0/SPI1_CSN0_M2/PWM2_CH3_M3/GPIO3_D0_d
165	GPIO2_B4_d	3.3V	GPIO(M2)	VI_CIF_D9/SDMMC1_PWREN_M1/ETH0_TXD2_M1/SAI0_SDI3_M0/PDM0_SDI0_M3/UART7_CTSN_M0/SPI4_MOSI_M3/SATA0_ACTLED_M0/GPIO2_B4_d
166	GPIO2_B2_d	3.3V	GPIO(4G 电源)	VI_CIF_D11/SDMMC1_CMD_M1/ETH0_TXD3_M1/SAI0_SDI2_M0/PDM0_SDI1_M3/UART1_CTSN_M1/SPI4_CSN0_M3/PCIE0_CLKREQN_M0/GPIO2_B2_d
167	GPIO3_B0_d	3.3V	GPIO(M2 电源)	VO_LCDC_D19/VO_EBC_SDCE3/ETH0_MCLK_M0/SAI4_MCLK_M1/DSMC_CSN1/FLEXBUS0_D8/UART10_RX_M0/SPI2_MOSI_M2/PWM0_CH0_M3/GPIO3_B0_d
168	GPIO0_A0_d	1.8V	GPIO(LCD 电源)	REF_CLK0_OUT/AUPLL_CLK_IN_M0/GPIO0_A0_d
169	GPIO2_B3_d	3.3V	GPIO(LCD 背光)	VI_CIF_D10/SDMMC1_CLK_M1/ETH0_TXCLK_M1/SAI0_SDO2_M0/PDM0_CLK1_M3/UART1_RTSN_M1/SPI4_CLK_M3/PCIE1_CLKREQN_M0/GPIO2_B3_d
170	GPIO0_B6_d	3.3V	GPIO(TP 复位)	SDMMC0_PWREN/SDMMC1_DETN_M2/HDMI_TX_HPDIN_M1/EDP_TX_HPDIN_M1/PWM1_CH2_M0/GPIO0_B6_d
171	GPIO0_B4_z	3.3V	GPIO(TP 中断)	REF_CLK1_OUT/I2C1_SCL_M1/UART4_TX_M2/PWM1_CH0_M0/GPIO0_B4_d
172	I2C2_SDA_M0_DECODE_RTC	3.3V	I2C2(Codec)	I2C2_SDA_M0/UART1_RX_M0/CPULIT_AVS/PWM1_CH3_M0/GPIO0_C0_d
173	I2C2_SCL_M0_DECODE_RTC	3.3V		I2C2_SCL_M0/UART1_TX_M0/NPU_AVS/PWM1_CH4_M0/GPIO0_B7_d
174	UART0_TX_M0_DEBUG	3.3V	UART0(DEBUG)	UART0_TX_M0/JTAG_TCK_M1/GPIO0_D4_u
175	UART0_RX_M0_DEBUG	3.3V		UART0_RX_M0/JTAG_TMS_M1/GPIO0_D5_u
176	GND		电源地	
177	PCIE1_REFCLKP	1.8V	PCIE	PCIE1_REFCLKP
178	PCIE1_REFCLKN	1.8V		PCIE1_REFCLKN
179	PCIE1_TXP	1.8V		PCIE1_TXP/SATA1_TXP/USB3_OTG1_SSTXP
180	PCIE1_TXN	1.8V		PCIE1_TXN/SATA1_TXN/USB3_OTG1_SSTXN
181	PCIE1_RXP	1.8V		PCIE1_RXP/SATA1_RXP/USB3_OTG1_SSRXP
182	PCIE1_RXN	1.8V		PCIE1_RXN/SATA1_RXN/USB3_OTG1_SSRXN
183	SDMMC1_D1_M0	1.8V	SDIO1(WIFI)	ETH1_RXD3_M1/SDMMC1_D1_M0/SAI3_LRCK_M1/I2C9_SCL_M1/SPI1_MOSI_M0/PWM1_CH1_M1/GPIO1_B5_d
184	SDMMC1_D0_M0	1.8V		ETH1_RXD2_M1/SDMMC1_D0_M0/SAI3_SCLK_M1/I2C9_SDA_M1/SPI1_CLK_M0/PCIE1_CLKREQN_M1/PWM1_CH0_M1/GPIO1_B4_d

185	SDMMC1_D2_M0	1.8V		ETH1_RXCLK_M1/SDMMC1_D2_M0/SAI3_SDO_M1/UART3_CTSN_M2/SPI1_MISO_M0/PCIE0_CLKREQN_M1/GPIO1_B6_d
186	SDMMC1_D3_M0	1.8V		ETH1_TXD2_M1/SDMMC1_D3_M0/SAI3_SDI_M1/UART3_RTSN_M2/SPI1_CSN0_M0/GPIO1_B7_d
187	SDMMC1_CMD_M0	1.8V		ETH1_TXD3_M1/SDMMC1_CMD_M0/PDM0_SDI2_M2/UART3_TX_M2/SPI1_CSN1_M0/PWM0_CH0_M1/GPIO1_C0_d
188	SDMMC1_CLK_M0	1.8V		ETH1_TXCLK_M1/SDMMC1_CLK_M0/SAI3_MCLK_M1/PDM0_CLK0_M2/UART3_RX_M2/GPIO1_C1_d
189	SDMMC0_DET_L	1.8V	SDIO0(TF 卡)	SDMMC0_DETN/SPI2_CSN1_M0/GPIO0_A7_u
190	SDMMC0_D1	3.3V		SDMMC0_D1/FSPI1_D1_M0/DSM_AUD_LN_M0/SAI3_MCLK_M3/UART0_TX_M1/UART7_TX_M2/I2C8_SDA_M0/SPI0_MISO_M1/CAN0_TX_M0/PWM2_CH3_M0/GPIO2_A1_d
191	SDMMC0_D0	3.3V		SDMMC0_D0/FSPI1_D0_M0/DSM_AUD_LP_M0/UART0_RX_M1/UART7_RX_M2/I2C8_SCL_M0/SPI0_MOSI_M1/CAN0_RX_M0/PWM2_CH2_M0/GPIO2_A0_d
192	SDMMC0_CLK	3.3V		SDMMC0_CLK/FSPI1_CLK_M0/SAI3_SCLK_M3/TEST_CLK_OUT/UART5_TX_M2/I2C5_SCL_M0/SPI0_CLK_M1/I3C1_SDA_PU_M1/GPIO2_A5_d
193	SDMMC0_CMD	3.3V		SDMMC0_CMD/FSPI1_CSN0_M0/SAI3_SDO_M3/UART5_RX_M2/I2C5_SDA_M0/SPI0_CSN0_M1/PWM2_CH4_M0/GPIO2_A4_d
194	SDMMC0_D3	3.3V		SDMMC0_D3/FSPI1_D3_M0/DSM_AUD_RN_M0/SAI3_SDI_M3/JTAG_TMS_M0/UART5_CTSN_M2/CAN1_TX_M0/I3C1_SDA_M1/GPIO2_A3_d
195	SDMMC0_D2	3.3V		SDMMC0_D2/FSPI1_D2_M0/DSM_AUD_RP_M0/SAI3_LRCK_M3/JTAG_TCK_M0/UART5_RTSN_M2/SPI0_CSN1_M1/CAN1_RX_M0/I3C1_SCL_M1/GPIO2_A2_d
196	SARADC_VIN1_KEY/RECOVERY	1.8V	烧写按键	SARADC_IN1
T1	MIPI_DPHY_CSI3_RX_D0N	1.8V	MIPI 摄像头	MIPI_DPHY_CSI3_RX_D0N
T2	MIPI_DPHY_CSI3_RX_D0P	1.8V		MIPI_DPHY_CSI3_RX_D0P
T3	MIPI_DPHY_CSI3_RX_D1N	1.8V		MIPI_DPHY_CSI3_RX_D1N
T4	MIPI_DPHY_CSI3_RX_D1P	1.8V		MIPI_DPHY_CSI3_RX_D1P
T5	MIPI_DPHY_CSI3_RX_D2P	1.8V		MIPI_DPHY_CSI3_RX_D2P/MIPI_DPHY_CSI4_RX_D0P
T6	MIPI_DPHY_CSI3_RX_D2N	1.8V		MIPI_DPHY_CSI3_RX_D2N/MIPI_DPHY_CSI4_RX_D0N
T7	MIPI_DPHY_CSI3_RX_D3P	1.8V		MIPI_DPHY_CSI3_RX_D3P/MIPI_DPHY_CSI4_RX_D1P
T8	MIPI_DPHY_CSI3_RX_D3N	1.8V		MIPI_DPHY_CSI3_RX_D3N/MIPI_DPHY_CSI4_RX_D1N

T9	MIPI_DPHY_CSI3_RX_CLKP	1.8V		MIPI_DPHY_CSI3_RX_CLKP
T10	MIPI_DPHY_CSI3_RX_CLKN	1.8V		MIPI_DPHY_CSI3_RX_CLKN
T11	MIPI_DPHY_CSI4_RX_CLKN	1.8V		MIPI_DPHY_CSI4_RX_CLKN
T12	MIPI_DPHY_CSI4_RX_CLKP	1.8V		MIPI_DPHY_CSI4_RX_CLKP
T13	PCIE0_TXP	1.8V	M2 电子盘	PCIE0_TXP/SATA0_TXP
T14	PCIE0_TXN	1.8V		PCIE0_TXN/SATA0_TXN
T15	PCIE0_RXN	1.8V		PCIE0_RXN/SATA0_RXN
T16	PCIE0_RXP	1.8V		PCIE0_RXP/SATA0_RXP
T17	PCIE0_REFCLKP	1.8V		PCIE0_REFCLKP
T18	PCIE0_REFCLKN	1.8V		PCIE0_REFCLKN
T19	eDP_TX_AUXP	1.8V	eDP 显示屏	DP_TX_AUXP
T20	eDP_TX_AUXN	1.8V		DP_TX_AUXN
T21	eDP_TX_D0N	1.8V		USB3_OTG0_SSRX2N/DP_TX_D2N
T22	eDP_TX _D0P	1.8V		USB3_OTG0_SSRX2P/DP_TX_D2P
T23	eDP_TX _D1P	1.8V		USB3_OTG0_SSTX2P/DP_TX_D3P
T24	eDP_TX _D1N	1.8V		USB3_OTG0_SSTX2N/DP_TX_D3N
T25	SARADC_VIN4	1.8V	ADC	SARADC_IN4
T26	SARADC_VIN5	1.8V		SARADC_IN5
T27	SARADC_VIN6	1.8V		SARADC_IN6
T28	SARADC_VIN7	1.8V		SARADC_IN7
T29	SARADC_VIN2_HW_ID	1.8V		SARADC_IN2

四、联系方式

总公司　　：广州佩特电子科技有限公司
总公司地址：广州市天河区大观中路新塘大街鑫盛工业园 A1 栋 201
总公司网站：http://www.gzpeite.net
SMT 子公司：广州佩特精密电子科技有限公司（全资子公司）
子公司地址：广州市白云区人和镇大巷村顺景路 11 号
SMT 网站　：http://www.gzptjm.com
官方淘宝店：https://shop149045251.taobao.com
微信扫描二维码联系支持人员：

广州佩特电子科技有限公司

2025 年 10 月